

DEC 20 2005

Attorney Docket No.: YOR920040091US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re PATENT APPLICATION of:

Puneet Gupta et al.

Appln. No.: 10/787,488

Filed: February 26, 2004

For: INTEGRATED CIRCUIT LOGIC WITH
SELF COMPENSATING BLOCK
DELAYS

Art Unit: 2814

Examiner: D. A. WILLE

AMENDMENT FAX TRANSMITTAL

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir,

Transmitted herewith are:

An Amendment (7 page)

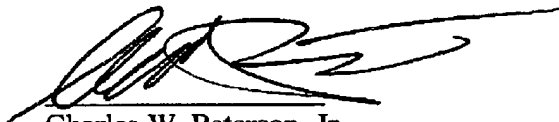
Total number of pages 8 (including this cover sheet)

Please charge any deficiencies in fees and credit any overpayment of fees to IBM Corporation Deposit Account no. 50-0510 and advise us accordingly.

I hereby certify that the above listed documents are being transmitted by fax to the Commissioner for Patents, United States Patent and Trademark Office, P.O. Box 1450 Alexandria, VA 22313-1450, at fax no. (571) 273-8300, this Tuesday, December 20, 2005.

December 20, 2005

(Date)



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Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to an Office Action dated November 28, 2005, please amend the
above-identified application as follows:

Amendments to the Claims begin on page 2 of this Amendment.

Remarks/Arguments begin on page 6 of this Amendment.